<u>**AMENDMENTS TO THE SPECIFICATION:**</u>

o   *Please replace the paragraph extending from page 8 line 26 to page 9 line 3 with the following:*

> Whether a lookup table 60 or an algorithm is used, the resulting phase figure 62 reflects a weighted influence of each of the k data bits on phase (or phase error). The ~~weighing~~ weighting factors are merely multipliers applied to each data bit. For an algorithm, the multipliers will be obvious. For a lookup table 60, the different multipliers are inherent in developing the lookup table 60 and are thus reflected in the output from the table. The different multipliers are preferably related to chronological proximity of each data bit to the most delayed sample, wherein more recent data bits (nearer the center of the register) are weighed heavier than early or late data bits (nearer the edges of the register).

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o   *Please replace the two consecutive paragraphs from page 10 line 18 to page 11 line ~~12~~ with the following:*

> It is a feature of GMSK modulation that $d\theta/dt$ inverts on every Q bit. Since the register 58 operates on alternating I and Q bits, a combination alternate bit switch 64 and ~~compliment~~ complement block 68 are imposed as shown to correct the sign of the Q bits (which are inverted for GMSK as above). The phase figure 62 (for example, the nth bit which corresponds to the most recent data bit in the register 58 being an in phase bit) passes directly through the alternate bit switch block 64 without modification. The alternate bit switch block 64 then switches to receive the next succeeding phase figure (for example, the $n^{th}+1$ bit which corresponds to the most recent data bit in the register 58 being a quadrature bit) from the ~~compliment~~ complement block 68. The alternate bit switch block 64 receives only one input at an given instant, alternating between an input directly from the lookup table 60 and an input from the ~~compliment~~ complement block 68. Since the example n^{th} bit was an in phase bit, the next succeeding $n^{th}+1$ bit is necessarily a quadrature component. The quadrature $n^{th}+1$ reconstructed phase passes through the